
Mauna Loa Solar Observatory Observer's Log

Fri Mar 30 17:17:15 GMT 2012

Year: 12 Doy: 090

Observer: berkey

Fri Mar 30 17:17:59 GMT 2012 H-ALPHA Start Patrol

WEATHER COMMENT: Fri Mar 30 17:17:23 GMT 2012

Clear sky, wind=20mph from the SE, temp=38F.

____end____

Fri Mar 30 17:19:24 GMT 2012 CHIP Start Patrol
Fri Mar 30 17:19:15 GMT 2012 MKIV Start Patrol
Fri Mar 30 17:25:29 GMT 2012: PSPT Start Patrol
Fri Mar 30 17:48:58 GMT 2012 MKIV End Patrol
Fri Mar 30 17:49:03 GMT 2012 MKIV Start Cal
Fri Mar 30 18:01:48 GMT 2012 CHIP LSD
Fri Mar 30 18:03:39 GMT 2012 CHIP End LSD
Fri Mar 30 18:03:53 GMT 2012 CHIP BiasLSD
Fri Mar 30 18:04:42 GMT 2012 CHIP End BiasLSD
Fri Mar 30 18:04:52 GMT 2012 CHIP Bias
Fri Mar 30 18:05:41 GMT 2012 CHIP End Bias
Fri Mar 30 18:05:48 GMT 2012 CHIP ReStart Patrol
Fri Mar 30 18:07:53 GMT 2012 MKIV End Cal
Fri Mar 30 18:08:03 GMT 2012 MKIV Start Patrol
Fri Mar 30 18:36:38 GMT 2012: PSPT Start Patrol
Fri Mar 30 20:04:12 GMT 2012 CHIP End Patrol
Fri Mar 30 20:05:48 GMT 2012 MKIV End Patrol
Fri Mar 30 20:06:33 GMT 2012 H-ALPHA End Patrol
NOTE BY BEN: Fri Mar 30 20:09:05 GMT 2012

Bottom shutter moved down

____end____

Fri Mar 30 20:09:15 GMT 2012 MKIV Start Patrol
Fri Mar 30 20:09:38 GMT 2012 H-ALPHA Start Patrol
Fri Mar 30 20:10:07 GMT 2012 CHIP Start Patrol
Fri Mar 30 20:19:21 GMT 2012 COMP Start Patrol
CoMP COMMENT BY BEN: Fri Mar 30 20:35:03 GMT 2012
CoMP auto-center-occulter code has been updated to new algorithm and appears to find the proper center.

____end____

MKIV COMMENT BY BEN: Fri Mar 30 22:28:49 GMT 2012
Akamai crashed with the emWaitRefEvent error

____end____

Fri Mar 30 22:29:20 GMT 2012 MKIV End Patrol
Fri Mar 30 22:51:23 GMT 2012 MKIV Start Patrol
Fri Mar 30 22:52:02 GMT 2012 MKIV Start Patrol
Fri Mar 30 23:00:31 GMT 2012 CHIP End Patrol
Fri Mar 30 23:07:43 GMT 2012 MKIV Start Patrol
Fri Mar 30 23:13:35 GMT 2012 CHIP Start Patrol
MKIV COMMENT BY BEN: Fri Mar 30 23:13:08 GMT 2012

Swapped vigra boards between alakai and akamai. Now MK4 has the good display board and chip has the suspect. A plan is in the works to remove the t_disp module from the chip code, if this works out alakai will go headless.

____end____

Fri Mar 30 23:27:28 GMT 2012 COMP End Patrol
Fri Mar 30 23:27:31 GMT 2012 COMP Start Patrol
Fri Mar 30 23:30:36 GMT 2012 COMP End Patrol
Fri Mar 30 23:35:11 GMT 2012 COMP Start Patrol
Fri Mar 30 23:55:04 GMT 2012 CHIP Start Patrol
Fri Mar 30 23:59:50 GMT 2012 CHIP Start Patrol
GONG COMMENT BY DARRYL: Sat Mar 31 00:32:05 GMT 2012
Despite 20+ mph winds from the SE the turret hasn't oscillated today.

____end____

CHIP COMMENT BY DARRYL: Sat Mar 31 00:34:07 GMT 2012
Note that after we put the CHIP Vigra video board into Mk4 we tried running CHIP w/o any Vigra board and the error messages looked the same as what we saw on Mk4 with the bad Vigra board. The flaky old Mk4 Vigra board worked OK in CHIP initially.

____end____

MKIV COMMENT BY DARRYL: Sat Mar 31 00:37:36 GMT 2012
I should also mention that Mk4 works well with the Vigra board from CHIP and we'll leave it in there for now.

____end____

Sat Mar 31 00:40:01 GMT 2012 CHIP End Patrol
Sat Mar 31 00:43:56 GMT 2012 CHIP Start Patrol
Sat Mar 31 00:50:51 GMT 2012 CHIP End Patrol
Sat Mar 31 00:55:59 GMT 2012 CHIP Start Patrol
Sat Mar 31 01:35:28 GMT 2012 COMP End Patrol
Sat Mar 31 01:35:49 GMT 2012 CHIP End Patrol
Sat Mar 31 01:36:25 GMT 2012 MKIV End Patrol
Sat Mar 31 01:38:03 GMT 2012 H-ALPHA End Patrol
Sat Mar 31 01:41:28 GMT 2012 MKIV Start Patrol
Sat Mar 31 01:41:48 GMT 2012 H-ALPHA Start Patrol
Sat Mar 31 01:42:18 GMT 2012 CHIP Start Patrol
Sat Mar 31 01:45:02 GMT 2012 COMP Start Patrol
Sat Mar 31 02:16:10 GMT 2012 CHIP End Patrol
Sat Mar 31 02:15:58 GMT 2012 COMP End Patrol
Sat Mar 31 02:16:13 GMT 2012: PSPT Abort Patrol
Sat Mar 31 02:18:33 GMT 2012 H-ALPHA End Patrol
Sat Mar 31 02:23:48 GMT 2012 MKIV End Patrol
Sat Mar 31 02:28:39 GMT 2012

MkIV

00_00.rawmk4	01_53.rawmk4	18_37.rawmk4	20_18.rawmk4	21_58.rawmk4
00_03.rawmk4	01_56.rawmk4	18_40.rawmk4	20_21.rawmk4	22_01.rawmk4
00_06.rawmk4	01_59.rawmk4	18_43.rawmk4	20_23.rawmk4	22_04.rawmk4
00_09.rawmk4	02_02.rawmk4	18_46.rawmk4	20_26.rawmk4	22_07.rawmk4
00_12.rawmk4	02_05.rawmk4	18_49.rawmk4	20_29.rawmk4	22_10.rawmk4
00_15.rawmk4	02_08.rawmk4	18_52.rawmk4	20_32.rawmk4	22_13.rawmk4

00_18.rawmk4	02_11.rawmk4	18_55.rawmk4	20_38.rawmk4	22_16.rawmk4
00_21.rawmk4	02_14.rawmk4	18_57.rawmk4	20_42.rawmk4	22_19.rawmk4
00_27.rawmk4	02_17.rawmk4	19_00.rawmk4	20_45.rawmk4	22_22.rawmk4
00_30.rawmk4	02_20.rawmk4	19_03.rawmk4	20_48.rawmk4	23_07.rawmk4
00_33.rawmk4	17_19.rawmk4	19_06.rawmk4	20_51.rawmk4	23_10.rawmk4
00_36.rawmk4	17_22.rawmk4	19_09.rawmk4	20_54.rawmk4	23_13.rawmk4
00_39.rawmk4	17_25.rawmk4	19_12.rawmk4	20_57.rawmk4	23_16.rawmk4
00_42.rawmk4	17_28.rawmk4	19_15.rawmk4	21_00.rawmk4	23_19.rawmk4
00_45.rawmk4	17_31.rawmk4	19_18.rawmk4	21_03.rawmk4	23_22.rawmk4
00_48.rawmk4	17_34.rawmk4	19_21.rawmk4	21_05.rawmk4	23_25.rawmk4
00_51.rawmk4	17_36.rawmk4	19_24.rawmk4	21_08.rawmk4	23_28.rawmk4
00_54.rawmk4	17_39.rawmk4	19_27.rawmk4	21_11.rawmk4	23_31.rawmk4
00_57.rawmk4	17_42.rawmk4	19_30.rawmk4	21_14.rawmk4	23_34.rawmk4
01_00.rawmk4	17_45.rawmk4	19_33.rawmk4	21_17.rawmk4	23_37.rawmk4
01_03.rawmk4	17_52.rawmk4	19_36.rawmk4	21_20.rawmk4	23_40.rawmk4
01_06.rawmk4	17_58.rawmk4	19_39.rawmk4	21_23.rawmk4	23_43.rawmk4
01_09.rawmk4	18_04.rawmk4	19_42.rawmk4	21_26.rawmk4	23_46.rawmk4
01_12.rawmk4	18_08.rawmk4	19_45.rawmk4	21_29.rawmk4	23_48.rawmk4
01_14.rawmk4	18_11.rawmk4	19_47.rawmk4	21_32.rawmk4	23_51.rawmk4
01_17.rawmk4	18_13.rawmk4	19_50.rawmk4	21_35.rawmk4	23_54.rawmk4
01_20.rawmk4	18_16.rawmk4	19_53.rawmk4	21_38.rawmk4	23_57.rawmk4
01_23.rawmk4	18_19.rawmk4	19_56.rawmk4	21_41.rawmk4	c17_49.rawmk4
01_26.rawmk4	18_22.rawmk4	19_59.rawmk4	21_44.rawmk4	c17_55.rawmk4
01_29.rawmk4	18_25.rawmk4	20_02.rawmk4	21_47.rawmk4	c18_01.rawmk4
01_41.rawmk4	18_28.rawmk4	20_09.rawmk4	21_50.rawmk4	
01_44.rawmk4	18_31.rawmk4	20_12.rawmk4	21_53.rawmk4	
01_47.rawmk4	18_34.rawmk4	20_15.rawmk4	21_55.rawmk4	