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Mauna Loa Solar Observatory Observer's Log  
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Tue Dec 29 17:17:28 GMT 1998

Year: 98 Doy: 363

Observer: yasukawa

WEATHER COMMENT: Tue Dec 29 17:18:19 GMT 1998

Cool, clear, light southeast breeze.

\*\*LOW-L PROBLEM\*\*: Tue Dec 29 17:18:54 GMT 1998

LOWL crashed sometime over week. Date and time reading

66/66/66 66:66:66. Stopped in sync mode, Files = 84 on drive 0.

Removed L00565 from drive 0. Loaded L00566 into drive 0 and L00567 into drive 1 and rebooted PC.

Tue Dec 29 17:26:13 GMT 1998 CHIP Startup--Initializing new tape

Tue Dec 29 17:27:34 GMT 1998 PICS Start Patrol

Tue Dec 29 17:28:12 GMT 1998 MKIII Start Patrol

Tue Dec 29 17:28:14 GMT 1998 CHIP CHIP Start Patrol

\*\*LOW-L PROBLEM\*\*: Tue Dec 29 17:43:33 GMT 1998

Cannot get CTS10 board to respond. Opening up PC to have a look.

Tue Dec 29 18:01:31 GMT 1998 PICS Flat

Tue Dec 29 18:01:49 GMT 1998 CHIP Bias

Tue Dec 29 18:02:44 GMT 1998 CHIP End Bias

Tue Dec 29 18:02:54 GMT 1998 CHIP Water

Tue Dec 29 18:03:01 GMT 1998 PICS End Flat

Tue Dec 29 18:03:35 GMT 1998 CHIP End Water

\*\*LOW-L PROBLEM\*\*: Tue Dec 29 18:16:02 GMT 1998

LOWL CTS10 board finally responding. Not sure what transpired exactly. Darryl's fix of checking the ctconfig did not work. Each time I checked port, message came up that the board was not responding to that port (277) and the time/date would display some nonsense data. Last ditch effort, I sent into setup. Missed the "press <del> key opportunity so I typed setup from dos. This is a different routine that prompts reloading of program from A: drive so I did a warm reboot to get out of it. This reboot worked and CTS10 board responded with the correct time and date.

Began LOWL observing program to L00566 in drive 0.

LOW-L COMMENT: Tue Dec 29 18:24:08 GMT 1998

During PC checkout, I cleaned the cts10 board contacts with wirebrush and alcohol. Lithium battery voltage still OK.

Tue Dec 29 18:26:20 GMT 1998 MKIII Start Cal

\*\*PICS PROBLEM\*\*: Tue Dec 29 18:36:59 GMT 1998

Halle temperature controller off, checking it out.

Tue Dec 29 18:46:31 GMT 1998 MKIII Start Patrol

Tue Dec 29 19:01:00 GMT 1998 CHIP Bias

Tue Dec 29 19:01:34 GMT 1998 PICS Flat

Tue Dec 29 19:01:54 GMT 1998 CHIP End Bias

Tue Dec 29 19:02:04 GMT 1998 CHIP Water

Tue Dec 29 19:02:42 GMT 1998 CHIP End Water

Tue Dec 29 19:03:00 GMT 1998 PICS End Flat

\*\*PICS PROBLEM\*\*: Tue Dec 29 19:29:34 GMT 1998

Problem is a failed AC power entry filter module. Sent email to KVS to try to locate a replacement.

GONG COMMENT: Tue Dec 29 19:54:29 GMT 1998

Going down to GONG to do weekly PM.

Tue Dec 29 20:00:49 GMT 1998 CHIP Gain

Tue Dec 29 20:01:37 GMT 1998 PICS Flat

Tue Dec 29 20:03:02 GMT 1998 PICS End Flat

Tue Dec 29 20:05:26 GMT 1998 CHIP End Gain

Tue Dec 29 20:05:37 GMT 1998 CHIP Bias

Tue Dec 29 20:06:26 GMT 1998 CHIP End Bias

Tue Dec 29 20:06:36 GMT 1998 CHIP Water

Tue Dec 29 20:07:11 GMT 1998 CHIP End Water

GONG COMMENT: Tue Dec 29 20:21:29 GMT 1998

Back from GONG. No spare powerline filter found.

Tue Dec 29 21:01:34 GMT 1998 PICS Flat

Tue Dec 29 21:01:50 GMT 1998 CHIP Bias

Tue Dec 29 21:03:01 GMT 1998 PICS End Flat  
 Tue Dec 29 21:03:01 GMT 1998 PICS End Flat  
 Tue Dec 29 21:03:10 GMT 1998 CHIP Water  
 Tue Dec 29 21:03:48 GMT 1998 CHIP End Water  
 \*\*PICS PROBLEM\*\*: Tue Dec 29 21:16:29 GMT 1998  
 Looked everywhere and could not find any unused equipment with a  
 powerline input filter at similar specs to temporarily cannibalize.  
 \*\*PICS PROBLEM\*\*: Tue Dec 29 22:00:28 GMT 1998  
 Jury rigged a Corcom filter into the Halle temperature controller temporarily  
 to supply unit with "filtered" power.  
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 Tue Dec 29 22:01:49 GMT 1998 CHIP Bias  
 Tue Dec 29 22:02:43 GMT 1998 CHIP End Bias  
 Tue Dec 29 22:02:53 GMT 1998 CHIP Water  
 Tue Dec 29 22:02:53 GMT 1998 CHIP Water  
 Tue Dec 29 22:03:41 GMT 1998 CHIP End Water  
 Tue Dec 29 22:08:38 GMT 1998 PICS End Patrol  
 Tue Dec 29 22:09:27 GMT 1998 CHIP CHIP End Patrol  
 Tue Dec 29 22:13:05 GMT 1998 MKIII End Patrol  
 Tue Dec 29 22:14:07 GMT 1998 CHIP ending tape  
 LOW-L COMMENT: Tue Dec 29 22:14:41 GMT 1998  
 LOWL time and date were correct each time I looked at it today  
 once it got going this morning.  
 COMMENT: Tue Dec 29 22:15:36 GMT 1998

Tapes:

MKIV: 98-363  
 PICS: P01362  
 CHIP: C00749  
 LOWL: L00566 in drive 0

Tue Dec 29 22:19:17 GMT 1998  
 MkIII

17_28.rawmk3	18_29.rawmk3	19_35.rawmk3	20_33.rawmk3	21_31.rawmk3
17_31.rawmk3	18_36.rawmk3	19_38.rawmk3	20_36.rawmk3	21_34.rawmk3
17_34.rawmk3	18_43.rawmk3	19_41.rawmk3	20_39.rawmk3	21_37.rawmk3
17_38.rawmk3	18_46.rawmk3	19_44.rawmk3	20_42.rawmk3	21_40.rawmk3
17_41.rawmk3	18_49.rawmk3	19_47.rawmk3	20_46.rawmk3	21_44.rawmk3
17_44.rawmk3	18_53.rawmk3	19_51.rawmk3	20_49.rawmk3	21_47.rawmk3
17_47.rawmk3	18_56.rawmk3	19_54.rawmk3	20_52.rawmk3	21_50.rawmk3
17_50.rawmk3	18_59.rawmk3	19_57.rawmk3	20_55.rawmk3	21_53.rawmk3
17_54.rawmk3	19_02.rawmk3	20_00.rawmk3	20_59.rawmk3	21_57.rawmk3
17_57.rawmk3	19_06.rawmk3	20_04.rawmk3	21_02.rawmk3	22_00.rawmk3
18_00.rawmk3	19_09.rawmk3	20_07.rawmk3	21_05.rawmk3	22_03.rawmk3
18_03.rawmk3	19_12.rawmk3	20_10.rawmk3	21_08.rawmk3	22_06.rawmk3
18_07.rawmk3	19_15.rawmk3	20_13.rawmk3	21_11.rawmk3	22_09.rawmk3
18_10.rawmk3	19_18.rawmk3	20_17.rawmk3	21_15.rawmk3	c18_26.rawmk3
18_13.rawmk3	19_22.rawmk3	20_20.rawmk3	21_18.rawmk3	c18_33.rawmk3
18_16.rawmk3	19_25.rawmk3	20_23.rawmk3	21_21.rawmk3	c18_39.rawmk3
18_20.rawmk3	19_28.rawmk3	20_26.rawmk3	21_24.rawmk3	
18_23.rawmk3	19_31.rawmk3	20_29.rawmk3	21_28.rawmk3	